

REMARKS

Applicant acknowledges receipt of the Examiner's Office Action dated January 22, 2007. This Office Action rejected all claims pending at that time. Specifically, Claims 28-41 were rejected under 35 U.S.C. §112, first paragraph, as containing subject matter not described in the Specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor, at the time the application was filed, had possession of the claimed invention. Claims 28-41 were rejected under 35 U.S.C. §112, first paragraph, as containing subject matter not described in the Specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Claims 28-41 were rejected under 35 U.S.C. §112, first paragraph, because the best mode contemplated by the invention has not been disclosed. In light of the foregoing amendments and following remarks, Applicants respectfully request the Examiner's reconsideration and reexamination of all pending claims.

The Office Action rejected all claims under 35 U.S.C. §112, first paragraph. This rejection focuses on Applicants' addition of the term "memory bus with" to the claims. Applicants respectfully traverse the rejections. Applicants assert that one of ordinary skill in the art would recognize that the ending address set forth in the claims can be calculated as a function of the memory bus coupled between the memory and the processor. Moreover, Applicants assert that one of ordinary skill in the art can recognize this feature by reading the Detailed Description of the subject invention. Notwithstanding this objection, Applicants have elected at this time to remove the phrase "memory bus with" from the claims. Applicants reserve the right to resubmit the claims, including the phrase "memory bus with," in a continuation application.

Applicants have amended the claims including independent Claims 28, 33, and 39.

Independent Claim 28 has been amended to recite the act of “transferring the data between the memory and the processor via a memory bus coupled therebetween, wherein the data is transferred two or more elements at a time.” Independent Claim 33 has been amended to recite “determining a quantity of data elements to be transferred in parallel between a memory and a processor via a memory bus coupled therebetween, wherein the quantity is determined from a width of the data elements to be transferred and a width of the memory bus.” Independent Claim 39 has been amended to include limitations similar to those added to independent Claim 33.

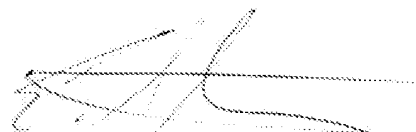
Applicant asserts support for the amendments to the independent claims can be found in the instant application on pages 26 and/or 27. As such, each of the independent claims find support within the Specification and is in compliance with 35 U.S.C. §112, first paragraph.

CONCLUSION

Applicant submits that all claims are now in condition for allowance, and an early notice to that effect is earnestly solicited. Nonetheless, should any issues remain that might be subject to resolution through a telephonic interview, the Examiner is requested to telephone the undersigned.

If any additional extensions of time under 37 C.F.R. § 1.136(a) are required in order for this submission to be considered timely, Applicant hereby petitions for such extensions. Applicant also hereby authorizes that any fees due for such extensions or any other fee associated with this submission, as specified in 37 C.F.R. § 1.16 or § 1.17, be charged to deposit account 502306.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'Eric A. Stephenson', is written over a horizontal dotted line.

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